

100

FIG. 1

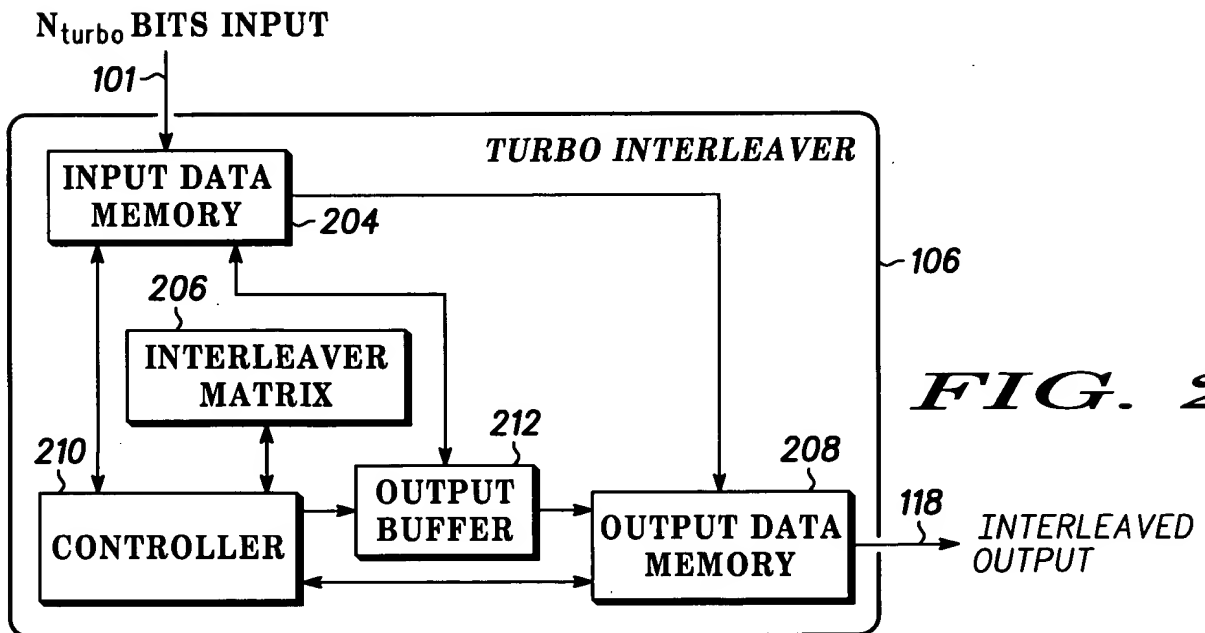


FIG. 2

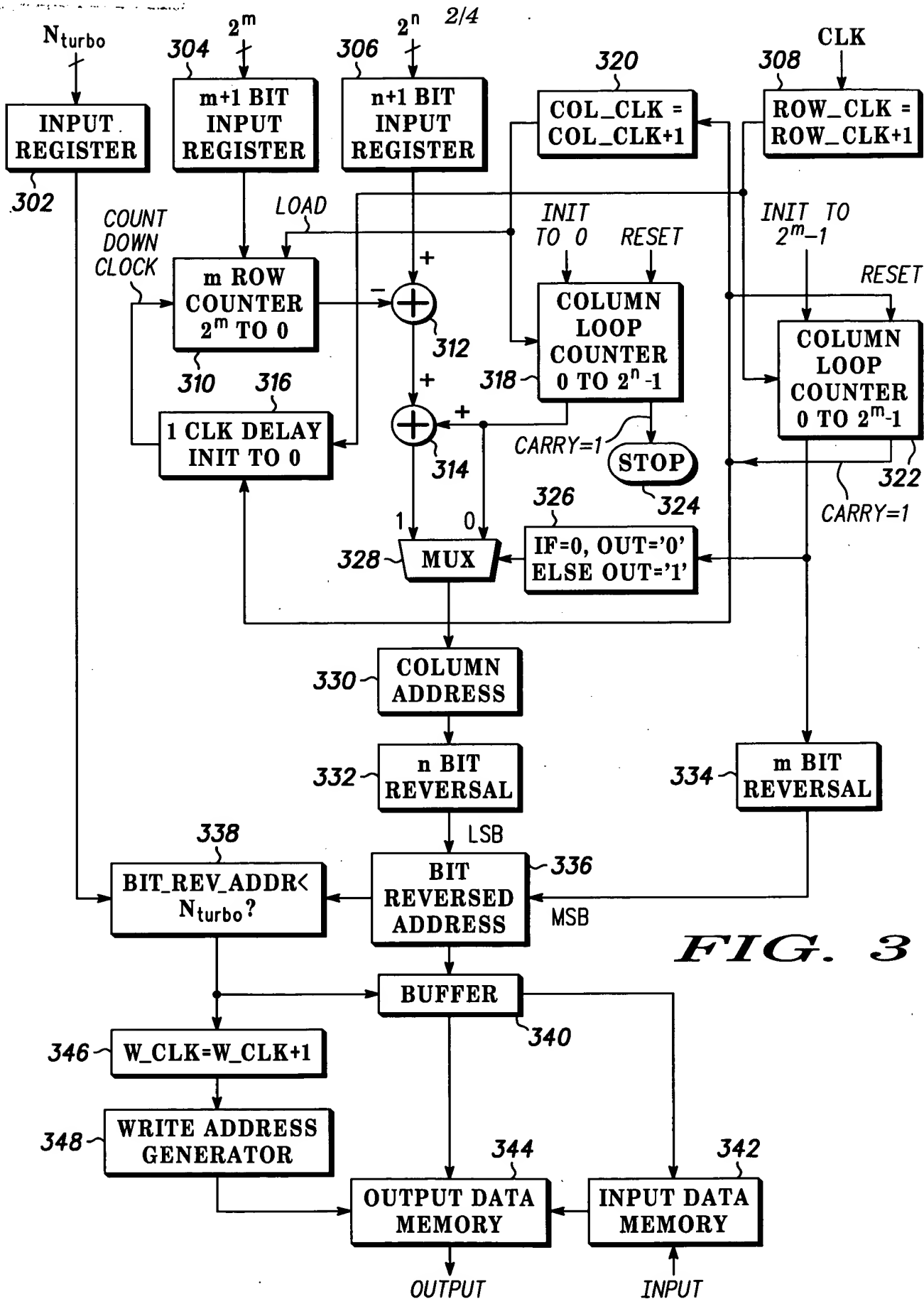
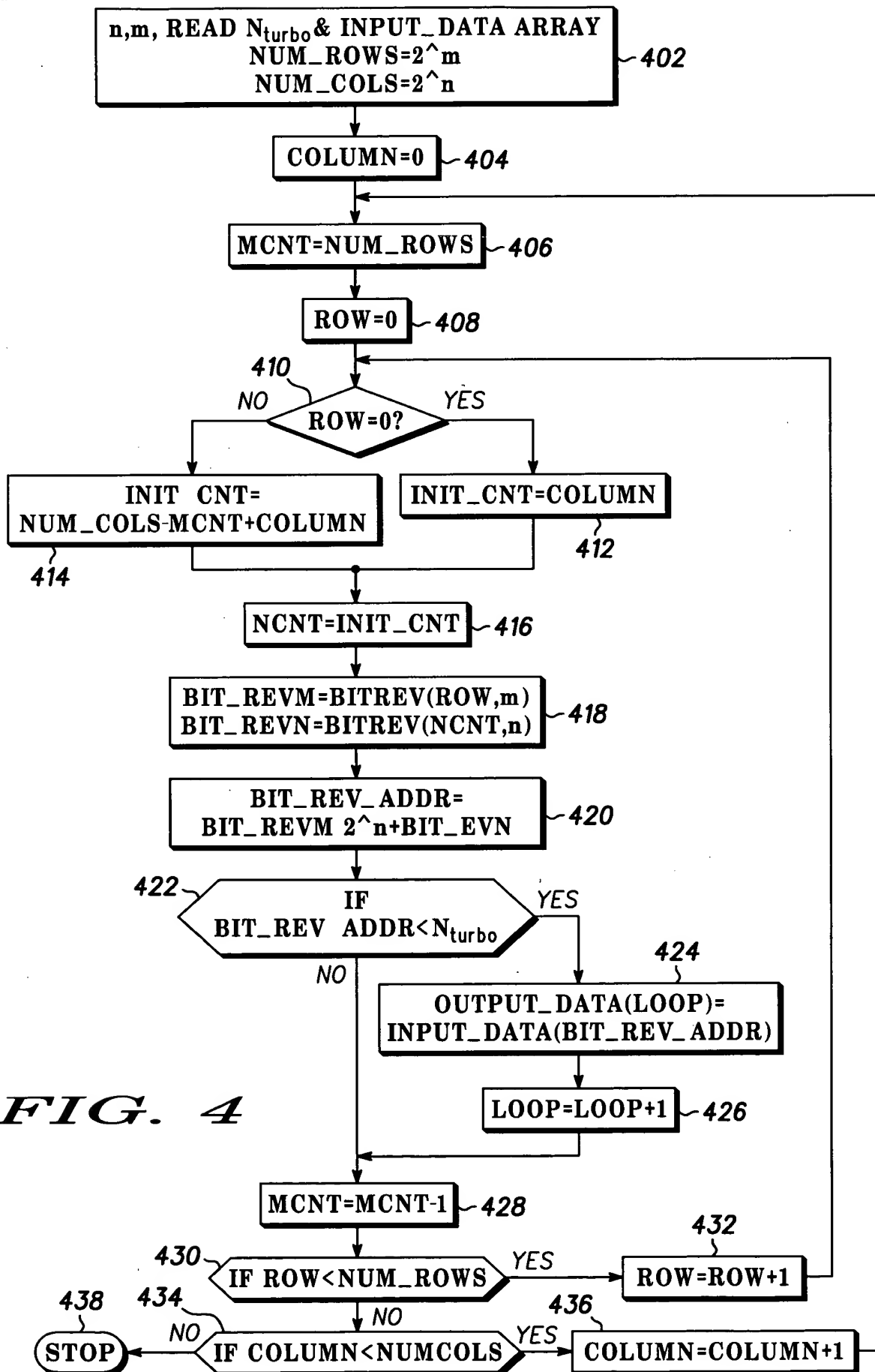


FIG. 3



INPUT INFORMATION
AND PARITY BITS

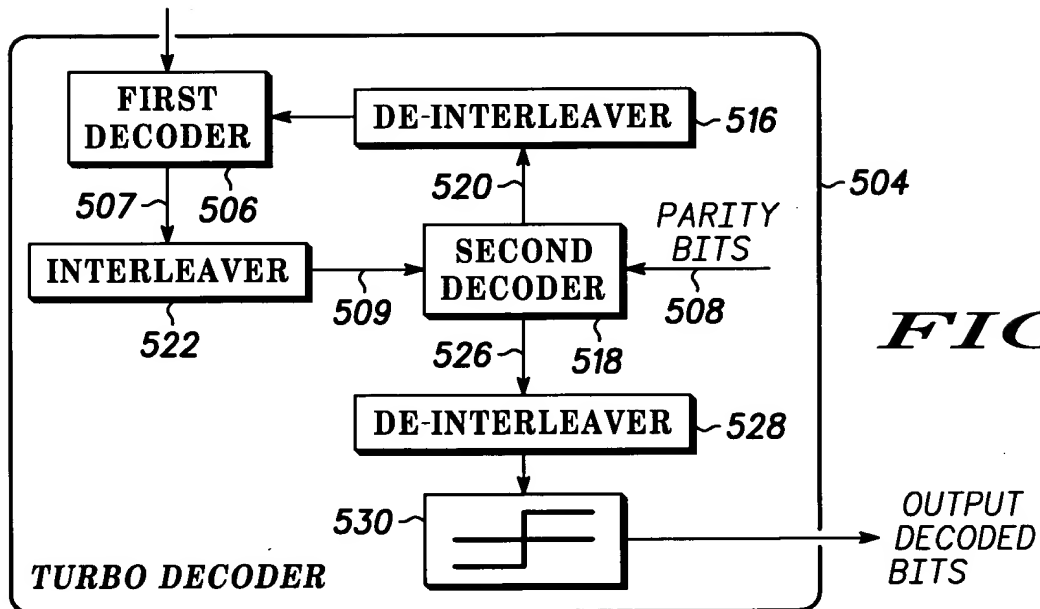


FIG. 5